

REMARKS

Claims 1-10, 13-18, 21, and 57-86 are now pending in this application. Claims 1, 4, 5, 15, 16 and 21 have been amended as set forth above to clarify the claimed invention, to overcome the 35 U.S.C. § 112 rejection, and/or to place this application in condition for allowance. Claims 11, 12, 19, 20 and 22-27 have been cancelled without prejudice to or disclaimer of the subject matter contained therein. Claims 28-56 were previously cancelled and are being pursued in a divisional application. Claim 1 now corresponds to original claim 12, claim 16 now corresponds to original claim 20, and new claim 69 corresponds to original claim 13. The Applicant respectfully reserves the right to pursue the original claims of this application and/or other claims in a continuation application.

The drawings (Figures 3 and 4) were objected to as being unreadable. The copy of the drawings in Applicant's file is readable. However, in order to provide further clarity to the drawings, two amended replacement drawing sheets have been submitted herewith. Withdrawal of the objection to the drawings in response to the submitted replacement sheets is respectfully requested.

Claims 4 and 5 were rejected under 35 U.S.C. § 112, second paragraph due to minor informalities. The Applicant respectfully submits that this rejection has been overcome by the amendments to claims 4 and 5 set forth above.

Therefore, withdrawal of the rejection under 35 U.S.C. § 112, second paragraph is respectfully requested.

Claim 15 was rejected under 35 U.S.C. § 112, second paragraph as being indefinite. It was not clear to the Examiner what is meant by “precharge artifacts”. The Applicant respectfully submits that the meaning of “precharge artifacts” is inherent to one of ordinary skill in the art. Precharge allows an amplifier to dynamically switch faster than its gate load would normally allow. Precharge has to be done in a way which does not affect the output. There are many complicated methods to implement this. However, according to some embodiments of the present invention, it is possible to realize the benefits of precharging while retaining the simplicity and stability of a static circuit. Put more simply, according to some embodiments, the output does not include evidence of precharging from the amplifier. Claim 15 has been amended to provide further clarity and simplicity to this feature. Therefore, withdrawal of the rejection under 35 U.S.C. § 112, second paragraph is respectfully requested.


Claims 1-3, 9-11, 14, and 16-19 were rejected under 35 U.S.C. § 102(b) as being anticipated by Patterson III (U.S. Patent No. 4,446,444). Claims 1-4, 9-11, 14, 16-19, 22, 25, 57, 60, 63 and 66 were rejected under 35 U.S.C. § 102(b) as being anticipated by Hu (U.S. Patent No. 5,990,708). Claims 5-8, 23, 24, 26, 27, 58, 59, 61, 62, 64, 65, 67 and 68 were rejected under 35 U.S.C. § 103(a) as being unpatentable over Hu in view of Greason et al. (U.S. Patent Number

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5,939,942). The Applicants respectfully submit that these rejections are now moot in view of the amendments to the claims set forth above. Therefore, withdrawal of the prior art rejections is respectfully requested.

In view of the foregoing, the application is considered to be in condition for allowance. Early notification of the same is earnestly solicited. If there are any questions regarding the present application, the Examiner is invited to contact the undersigned attorney at the telephone number of 815-885-2389.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Robert D. Anderson', written over a horizontal line.

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Date

Intel Americas, Inc.